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Arithmetic for Computers

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- Exponent \Rightarrow no. of positions to move the point in the fraction

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Advantages of Normalized Scientific Notation

- Simplifies exchange of floating point data
- Simplifies arithmetic
- Increases accuracy: unnecessary leading 0's are replaced by real numbers on the right

• Binary point (analogous to *decimal* point)

 $1.101_{two} \times 2^{-4}$

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- Why 1 in fraction?
- (Will use exponent in decimal for simplicity)

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Binary Floating Numbers

• In design: compromise between sizes of *fraction* and *exponent*

- between *precision* and *range*
- since fixed word size

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- Represent in (floating) binary word as:

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- S (sign bit): 1 bit (31st bit)
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- Not just MIPS formats: IEEE 754 floating-point standard

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- Underflow: Too accurate to represent
 - Negative exponent too large to fit

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- Increased range:

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¹as represented in the word

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• Final representation:

$$(-1)^S\times(1+F)\times 2^{(E-127)}$$

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MIPS Instruction support for floating point numbers

• To load into memory (.data section)

- .float number₁
- .double number₂
- Floating-point registers:
 - \$f0, \$f1, \$f2, ...
 - Use couples for double
- To load & store from memory
 - lwc1 \$f0, 0(\$t1) or lwc1 \$f0, num_var
 - swc1\$f2,0(\$t2)
- For arithmetic (single precision)
 - add.s, sub.s, mul.s, div.s
 - add.d, sub.d, mul.d, div.d